What is claimed is:

1. A servo track writer comprising:

a digital signal processor; and,

a clock head which reads a clock track from a disk surface to generate a clock signal that is used to provide interrupt signals to the digital signal processor.

2. The servo track writer of claim 1, wherein the digital signal processor positions a transducer that writes spiral servo information onto the disk surface.

3. The servo track writer of claim 1 further including a divide-by-M circuit to generate interrupt signals at an interrupt rate that is tied to the disk surface.

4. The servo track writer of claim 3, wherein the interrupt rate is equal to a predetermined servo sample rate.

5. The servo track writer of claim 3, wherein a predetermined servo sample rate divided by the interrupt rate equals a natural number.

6. The servo track writer of claim 1 further including a clock head amplifier which amplifies the clock signal.

7. The servo track writer of claim 6 further including a phase-locked loop which receives the amplified clock signal and generates a digital clock signal.

8. The servo track writer of claim 7 further including a divide-by-M circuit to divide down the digital clock signal, wherein the digital clock signal is provided to the digital signal processor.

9. The servo track writer of claim 1, wherein a predetermined number of servo sectors per revolution are designed to placed on the disk surface, wherein a predetermined number of interrupt signals are provided to the digital signal processor per revolution of the disk surface, and wherein the predetermined number of servo sectors per revolution divided by the predetermined number of interrupt signals provided to the digital signal processor per revolution equals a natural number.

10. The servo track writer of claim 1 further including:

a crystal which provides interrupt signals to the digital signal processor; and,

a switch to selectively provide interrupt signals to the digital signal processor from the clock head and the crystal.

11. A method of writing servo information onto a disk surface using a servo track writer having a digital signal processor comprising the steps of:

reading a clock track written onto the disk surface to generate interrupt signals; and,

providing the interrupt signals to the digital signal processor.

12. The method of claim 11, wherein the servo information is written in spiral patterns.

13. The method of claim12 including the step of:

positioning a transducer over the disk surface under control of the digital signal processor.

14. The method of claim 13, wherein the transducer is positioned based upon the interrupt signals provided to the digital signal processor.

15. The method of claim 12 including the step of:

generating a spiral profile based upon a predetermined interrupt rate.

16. The method of claim 15, wherein the profile includes a write portion, a post-write pad portion, a re-trace portion and a post-re-trace pad portion.

17. The method of claim 15 including the step of:

writing one of the spiral patterns onto the disk surface by following the spiral profile.

18. The method of claim 15 including the step of:

writing the spiral patterns onto the disk surface by following a series of spiral profiles.

19. The method of claim 12, wherein the spiral patterns are written sequentially about the disk surface.

20. The method of claim 12, wherein the spiral patterns are written in a random manner about the disk surface.

21. The method of claim 12, wherein the spiral patterns include synch marks and wherein synch marks along a particular radius are equally spaced.

22. The method of claim 12, wherein the spiral patterns include synch marks and wherein corresponding synch marks along different spirals are located on common radiuses.

23. A servo track writer comprising:

a digital signal processor;

a clock head which reads a clock track from a disk surface to generate an analog clock signal;

a clock head amplifier for amplifying the analog clock signal to generate an amplified clock signal;

circuitry for converting the amplified clock signal to a digital clock signal which is delivered to the digital signal processor to provide interrupt signals thereto.

24. The servo track writer of claim 23 further including circuitry to divide down the digital clock signal before it is delivered to the digital signal processor.

25. The servo track writer of claim 23, wherein the digital signal processor positions a transducer that writes spiral servo information onto the disk surface based upon the interrupt signals delivered thereto.

26. The servo track writer of claim 23 further including:

a crystal which provides interrupt signals to the digital signal processor; and,

a switch to selectively provide interrupt signals to the digital signal processor from the clock head and the crystal.